



#61IDS  
Dlungs  
4-1-03

2821  
JF

P/10-584

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of  
Robert C. NDWMAN Jr. et al.  
Serial No.: 10/0006,036  
Filed: December 5, 2001

New York, New York  
Date: March 12, 2003  
Group Art Unit: 2821  
Examiner: A. Minh

RECEIVED  
MAR 26 2003  
TECHNOLOGY CENTER 2800

For: SINGLE SWITCH ELECTRONIC DIMMING BALLAT

Assistant Commissioner for Patents  
Washington, D.C. 20231

SUBMISSION

Sir:

Submitted herewith is a copy of art together with a form listing the same for the convenience of the Examiner.

I respectfully request that the information submitted be considered and enclose our check number 15359 in payment of the required \$180.00 fee.

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed or if any additional fee due during the pendency of this application is not paid, the Patent and Trademark Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231, on March 12, 2003

Louis C. Dujmich

Respectfully submitted,

Louis C. Dujmich  
Registration No.: 30,625  
OSTROLENK, FABER, GERB & SOFFEN, LLP  
1180 Avenue of the Americas  
New York, New York 10036-8403  
Telephone: (212) 382-0700

Name of applicant, assignee or  
Registered Representative

Signature

March 12, 2003

Date of Signature

Adjustment date: 03/24/2003 CMQUYEN  
03/20/2003 HLE333 00000041 10006036  
01 FC:1806 LCD:cfm 180.00 DP  
Enclosures

03/20/2003 HLE333 00000041 10006036  
01 FC:1806 180.00 DP

BEST AVAILABLE COPY

03/24/2003 CMQUYEN 00000004 10006036  
01 FC:1806 180.00 DP

| APPLICANT'S ART CITATION<br>(Use several sheets if necessary)   |                 |   |   |                 |   |   | Application 10/006,036                 | OFGS File No. P/10-584 |           |           |                            |    |  |
|---|-----------------|---|---|-----------------|---|---|--|------------------------|-----------|-----------|----------------------------|----|--|
|   |                 |   |   |                 |   |   | Applicant Robert C. NEWMAN Jr., et al. |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   | Filing Date December 5, 2001           |                        |           |           | Group Art Unit 2821        |    |  |
| U.S. PATENT DOCUMENTS   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
| Examiner Initial  | Document Number |   |   |                 |   |   | Date                                   | Name                   | Class     | Sub-class | Filing Date If Appropriate |    |  |
|   | 6               | 0   | 6 | 1               | 2 | 5 | 9                                      | 5/00                   | DeMichele |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
| FOREIGN PATENT DOCUMENTS  |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   | Document Number |   |   |                 |   |   | Date                                   | Country                | Class     | Sub-class | Translation                |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           | Yes                        | No |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)  |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 | "Modified Valley Fill High Power Factor Electronic Ballast For Compact Fluorescent Lamps"   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 | Mustanir H. Kheraluwala, Sayed Amr El-Hamamsy G.E. R&D 1995 IEEE pp.10-14   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 | "Mosfet Switch Provides Efficient ac/dc Conversion" Spehro Pefehny, Design Ideas XP-000966357   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 | "Evaluation of a Novel Single-Stage High-Power-Factor Electronic Ballast Based on Integrated Buck Half-Bridge Resonant Inverter" J.M. Alonso, A.J. Calleja, J.Ribas,          |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 | E. Corominas and M. Rico-Secades - Universidad de Oviedo, DICECS - Technology Electronica - 2000 IEEE - pp. 610-616   |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 | "A New Discharge Lamp Ballast Based on a Self-Oscillating Full-Bridge Inverter Integrated with a Buck Type PFC Circuit" - J.Ribas, J.M. Alonso, A.J. Calleja, E.L. Corominas, |   |                 |   |   |  |                        |           |           |                            |    |  |
|   |                 | M. Rico-Secades - Universidad de Oviedo - dpto. De Ingenieria Electrica y Electronica Technologia Electronica - 2001 IEEE pp. 688-694.  |   |                 |   |   |  |                        |           |           |                            |    |  |
| Examiner  |                 |   |   | Date Considered |   |   |  |                        |           |           |                            |    |  |
| EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant. |                 |   |   |                 |   |   |  |                        |           |           |                            |    |  |

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

# BEST AVAILABLE COPY